

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS

PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTXp1	Positive half of first SuperSpeed TX differential pair	B11	SSTXp1	Positive half of first SuperSpeed TX differential pair
A3	SSTXn1	Negative half of first SuperSpeed TX differential pair	B10	SSTXn1	Negative half of first SuperSpeed TX differential pair
A4	Vbus	Bus Power	B9	Vbus	Bus Power
A5	CC1	Configuration Channel	B8	CC1	Configuration Channel
A6	Dp1	Positive half of the USB 2.0 differential pair—position 1	B7	Dp1	Positive half of the USB 2.0 differential pair—position 1
A7	Dn1	Negative half of the USB 2.0 differential pair—position 1	B6	Dn1	Negative half of the USB 2.0 differential pair—position 1
A8	RFU1	Reserved for Future Use (RFU)	B5	RFU1	Reserved for Future Use (RFU)
A9	Vbus	Bus Power	B4	Vbus	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSRXn2	Negative half of second SuperSpeed RX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSRXp2	Positive half of second SuperSpeed RX differential pair
A12	GND	Ground return	B1	GND	Ground return

RECOMMEND PCB LAYOUT
(THICKNESS 1.00 OR 1.20mm)

RoHS Compliant

TOLERANCES ARE

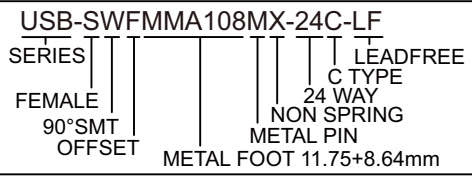
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DRAWING BY CY
CHECKED BY GENIUS
UNIT / mm SCALE 1 : 1
DATE
SIZE A4 PPROJECTION

X, ±
.X ± 0.30
.XX ± 0.20
.XXX ± 0.10
ANG ± 1°



ORDER INFORMATION



DESCRIPTION: USB3.1 C-TYPE SMT