

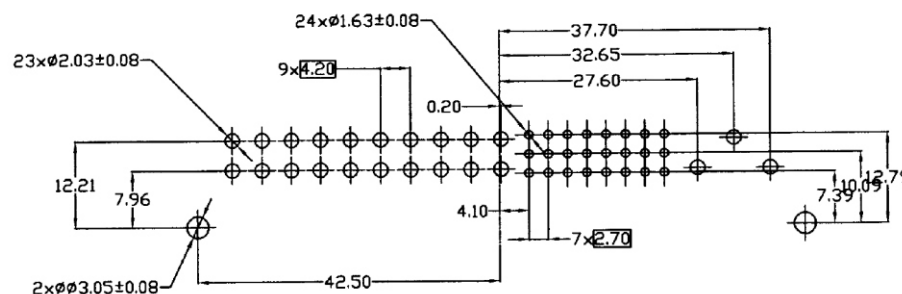
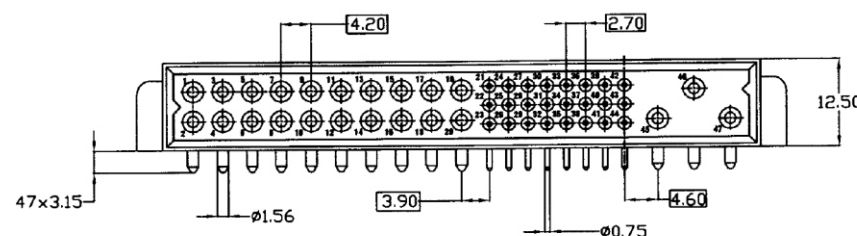
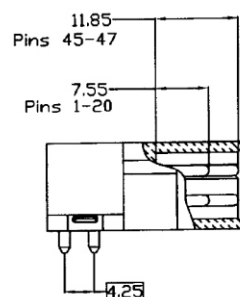
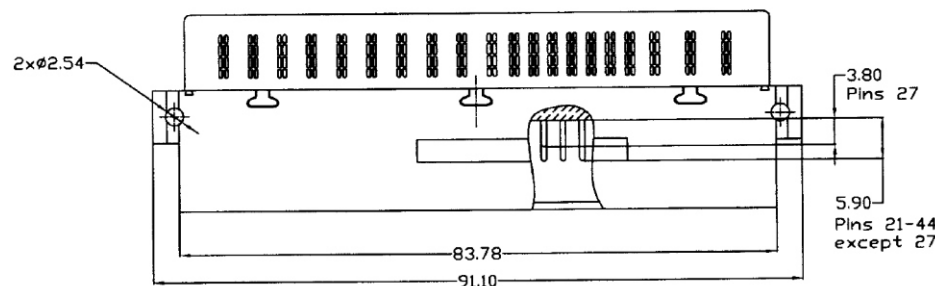
Notes:

- Pin contacts to be inserted into insulator in direction shown prior to bending.
- Contacts must withstand a pullout force of 2.50 Kgf min per power contact and 1.0 Kgf min per signal contact
- Bend contacts in direction shown. Note orientation to position 1.
- Materials and finish:
(1.) Insulator: High-Temperature Thermoplastic, UL94V-0 Color: Black
(2.) Power and Signal Pins: Copper alloy.
- Electrical Characteristics
Insulation Resistance: $\geq 10^{10}$ ohms
Initial Contact Resistance:
Power Contact: 0.7 milliohms max
Signal Contact: 4.0 milliohms max
Voltage Proof:
Power 1 through 20: 1500 Vrms
Power 45, 46, 47: 3000 Vrms
Signal 21 through 44: 1000 Vrms
Current Rating:
DC Output Power 1 through 20: 16 Amps
Input power 45, 46, 47: 23 Amps
Signal contacts: 2 Amps
Minimum creepage distance:
Power 47 to Contact 45: 3.2mm
Power 46 to Contact 45: 3.2mm
Power 47 to Signal contacts: 6.4mm
Power 46 to Signal contacts: 6.4mm
Power 47 to Contact 46: 2.5mm
Power 45 to Signal contacts: 2.0mm
- PC Board thickness:
Minimum: 1.60mm nominal
Maximum: 3.00mm nominal
- RoHS Directive Compliance
- Finish: plating gold on contact area. Matte tin in tail area. All over nickel on entire contact.
- Finish: plating gold on contact area. Gold in tail area. All over nickel on entire contact.

CPCI-PR-47W23-LF

LEADFREE

RoHS Compliant



RECOMMENDED PC BOARD REQUIREMENTS
TOLERANCES: ± 0.05 mm

(COMPONENT SIDE OF BOARD SHOWN)

MORE THAN ALL
CONNECTORS
ASSEMBLIES

PART NO. CPCI-PR-47W23

DWG NO. CPCI-PR-47W23

FILE NO.

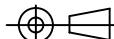
UNIT / mm

SCALE 1:1

CHECKED BY CY

DRAWING BY

PROJECTION



TOLERANCES ARE

.X $\pm$ 0.2

.XX $\pm$

.XXX $\pm$

AWG

DESCRIPTION:

AREA

REVISIONS

HK

DATE

焯倫股份有限公司