

RECOMMEND PCB LAYOUT
GENERAL TOLERANCE ±0.05

SPECIFICATION

1. Material:

housing: LCP, UL94V-0,color:black.
orbit: PA46, UL94V-0,color:black.
Contacts: Phosphor Bronze,
Tin 160u" at Solder Tail,
Selected Gold on Contact Area Plating.
Shell: Stainless. pickling

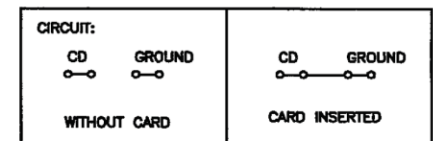
2. Electrical Characteristics:

Operating voltage: 100V AC(rms)/DC.
Current rating: 0.5A.
Operating Temperature: -25°C~+85°C.
Insulation resistance: 1000M ohms min. at 500 VDC
Dielectric withstanding voltage:500 VAC/1 minute.
Contact resistance: 100m ohms max.

3. Mechanical characteristics

3.1 Mating durability: 10000 cycles.
3.2 Insertion force: 2.0 Kgf Max.
3.3 Extraction force: 0.5 Kgf Min.
3.4 Mating cycles: 10000 cycles at the speed rate of 400-600 cycles/h; Without distortion of plastic and serious wear and tear in terminal, No signal break.

Pin No.	MICRO SIGNAL	DESCRIPTION
PIN1	DAT2	1P
PIN2	CD/DAT3	2P
PIN3	CMD	3P
PIN4	VDD	4P
PIN5	CLK	5P
PIN6	VSS	6P
PIN7	DAT0	7P
PIN8	DAT1	8P
PIN9	CARD DETECT	



MORETHANALL
PCB CONNECTORS
Cable ASSEMBLIES
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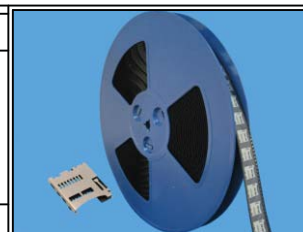
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RoHS Compliant

DRAWING BY CY
CHECKED BY GENIUS
UNIT / mm SCALE 1 : 1
DATE
SIZE A4 PPROJECTION

TOLERANCES ARE

X, ±
.X ± 0.25
.XX ± 0.15
.XXX ±
ANG ±



ORDER INFORMATION

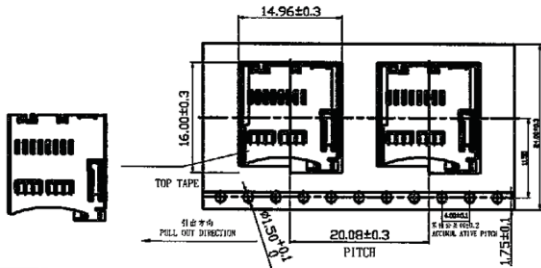
TFC-WPCE13BR-08-LF
SERIES | | | | | LEADFREE
90°SMT | | | | | 08WAY
PUSH PUSH | | | | | REEL
SWITCH OPEN
EAR 13.63mm
C/LAYOHT

DESCRIPTION:

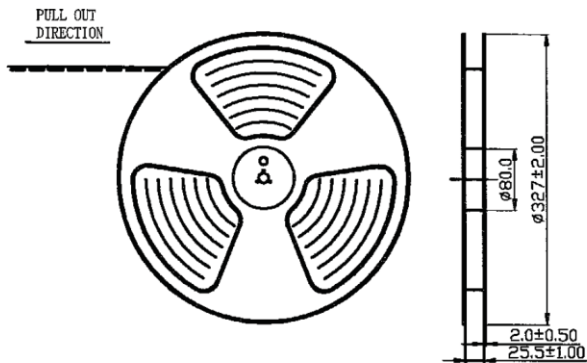
包裝作業圖示及說明

(PACKING OPERATION DIAGRAM & INSTRUCTION)

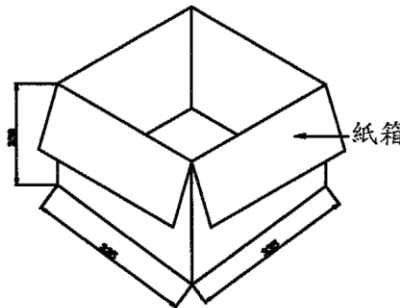
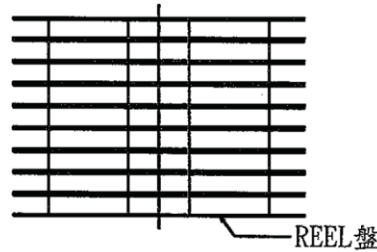
- 一.
- (1)將成品一一放入REEL包裝盤內,依同一方向放入.
 - (2)包裝時,如圖所示.
 - (3)一個REEL包裝盤放置1000個成品.



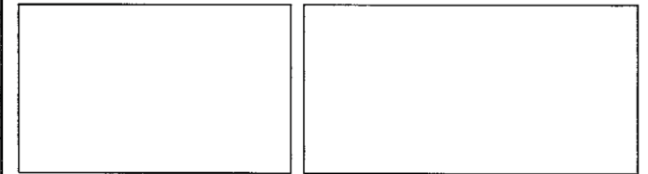
- 二.
- (1)裝盤前先把前面空800mm,然後再開始裝盤,尾端需空出400mm.
 - (2)裝滿成品的REEL包裝盤如下圖所示.



- 三.
- (1)每箱裝10盤REEL包裝盤.
 - (2)每箱放置10000PCS的成品.



- 四.
- (1)用TAPE將紙箱封實.



備註 (REMARK)

1. 若有未裝滿之零數箱,必須以緩衝材塞滿.

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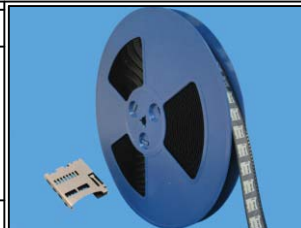
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